

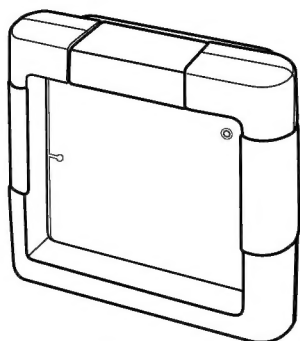
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The diagram illustrates the first 12 rows of Pascal's triangle. Each row is composed of squares representing the binomial coefficients. The rows are labeled 1 through 12 on the left side.

Row	Squares
1	□ □ □ □ □ □ □
2	□ □ □ □ □ □ □ □
3	□ □ □ □ □ □ □ □ □ □
4	□ □ □ □ □ □ □ □ □
5	□ □ □ □ □ □ □ □ □ □
6	□ □ □ □ □ □ □ □ □
7	□ □ □ □ □ □ □ □ □ □ □
8	□ □ □ □ □ □ □ □ □ □ □
9	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
10	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
11	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □
12	□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

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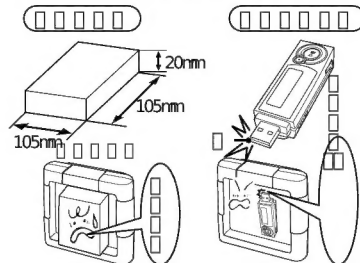
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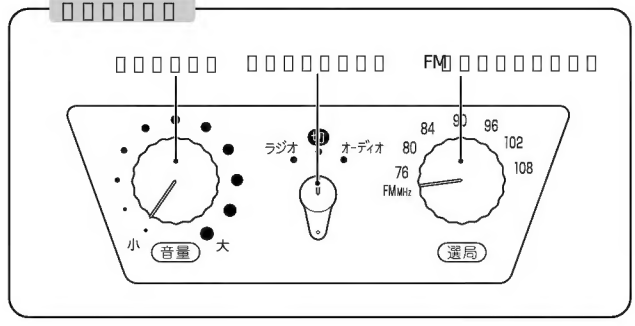
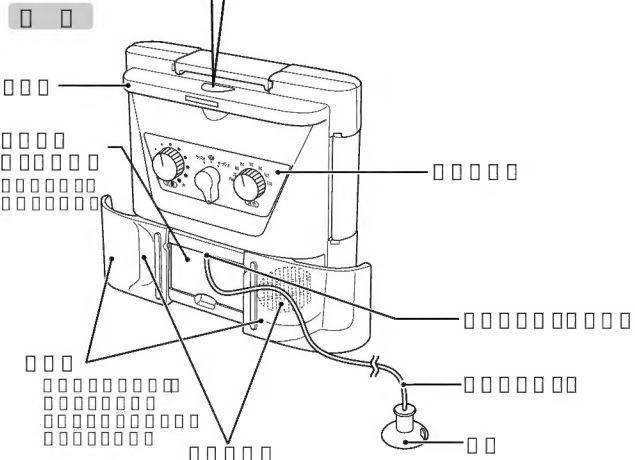
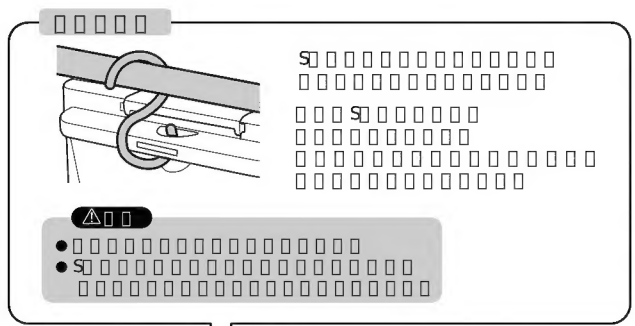
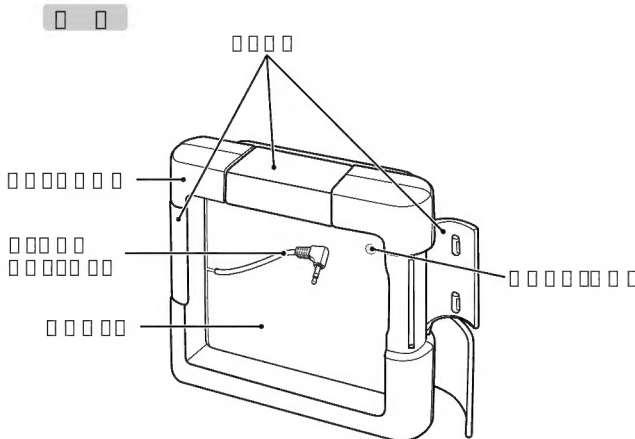
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Student	iPods
John	18
Mary	12
Sue	10
iPod	20

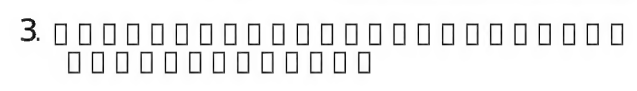
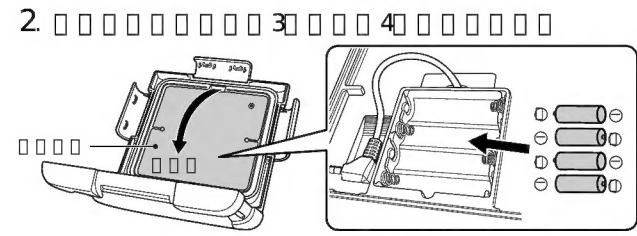
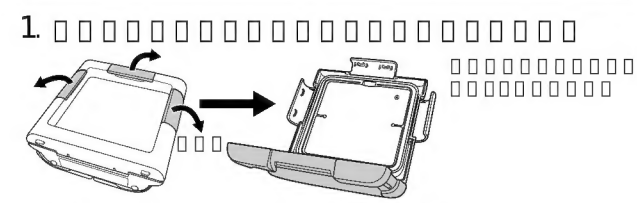
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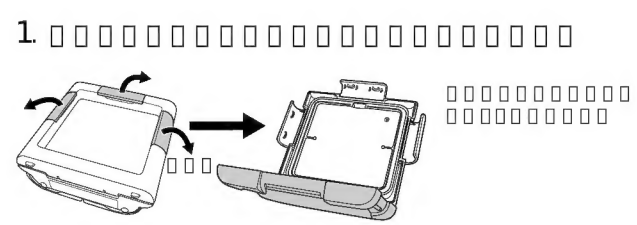
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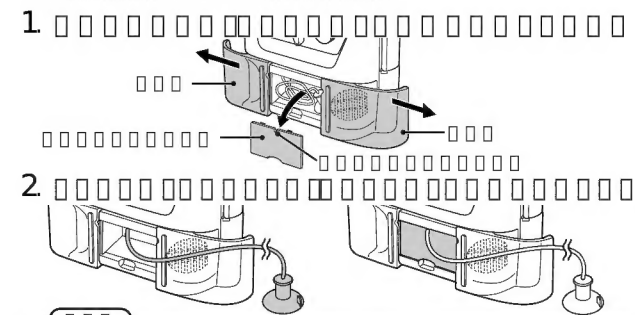
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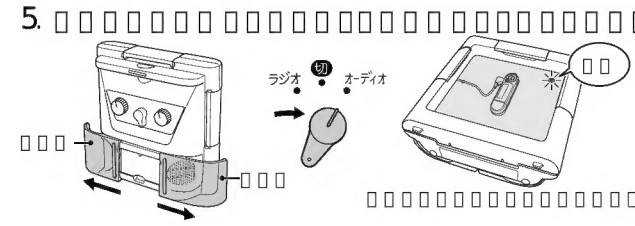
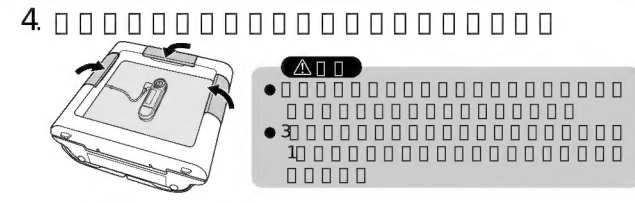
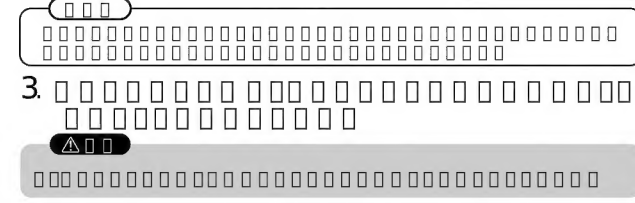
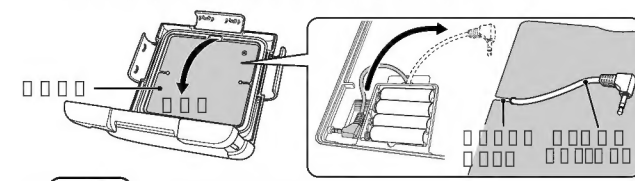
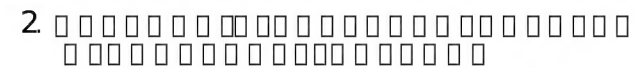
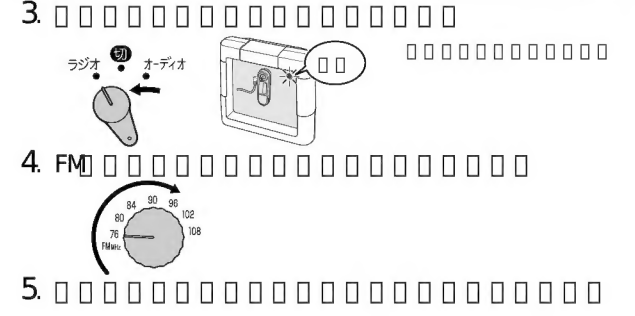


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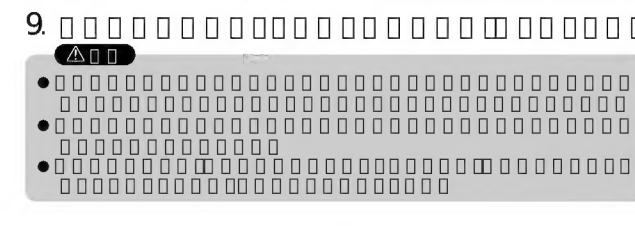
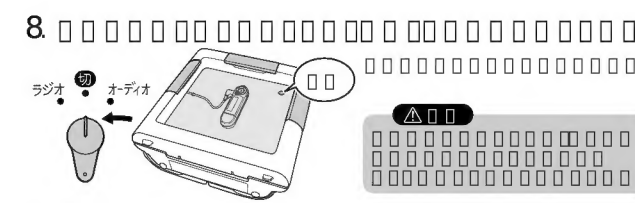
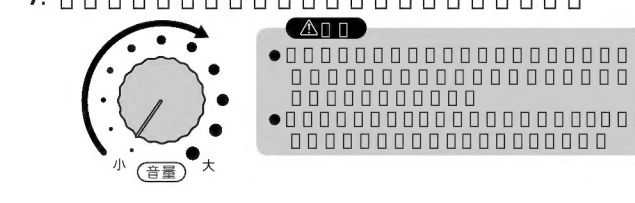
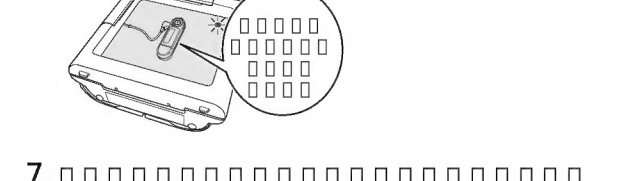
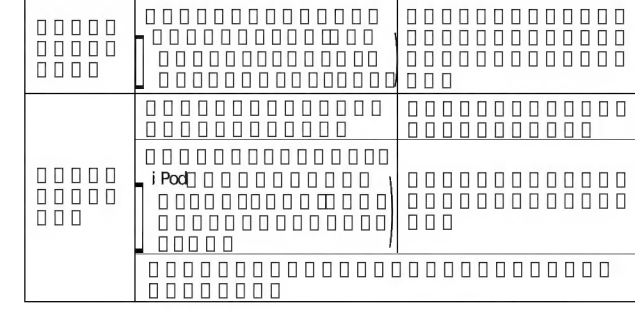
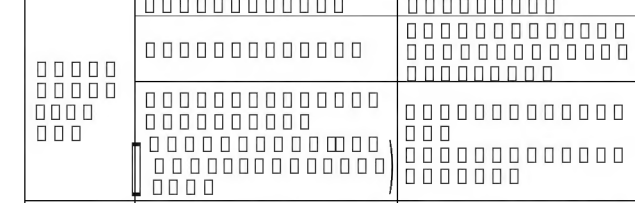
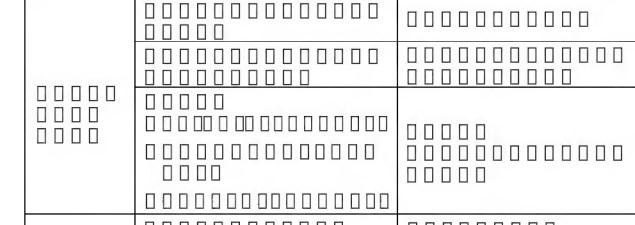


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The diagram illustrates the internal architecture of the 8086 microprocessor. At the top, a row of 20 pins is shown. Below this, the internal components are organized into several blocks:

- ALU (Arithmetic Logic Unit):** A large block on the left containing the ALU itself, the 8086 instruction decoder, and the 8086 control logic. It includes a 12-bit ALU, a 12-bit register, and a 12-bit ALU control logic.
- Registers:** A central block containing the 8086 register file, which includes the 8086 instruction register, the 8086 instruction pointer, and the 8086 instruction queue.
- Control Logic:** A block on the right containing the 8086 control logic, which includes the 8086 instruction decoder, the 8086 control logic, and the 8086 instruction queue.

The diagram also shows the internal structure of the ALU, including the 8086 ALU, the 8086 register, and the 8086 ALU control logic. The ALU is a 12-bit ALU, the register is a 12-bit register, and the ALU control logic is a 12-bit ALU control logic.

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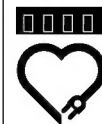
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**QUESTION**

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